

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:)	
)	
Benjamin S. Ting)	Art Unit: Not yet assigned
)	
Serial No.: Not yet assigned)	Examiner: Not yet assigned
)	
Filed: Herewith)	
)	
For: Architecture and Interconnect)	
Scheme for Programmable Logic)	
Circuits)	
)	
<hr/>		
a Continuation of:)	
)	
Serial No. 10/269,364)	
)	
Filed: October 11, 2002)	
)	
<hr/>		

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to the examination of the above-referenced application on the merits it is respectfully requested that the following amendments be entered.